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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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In re application of: Kota, et al.

Attorney Docket No.: NWISP045

Application No.: 10/602,280

Examiner: Brendan Y. Higa

Filed: 06-23-2003

Group: 2153

Title: BANDWIDTH, FRAMING AND ERROR  
DETECTION IN COMMUNICATIONS  
BETWEEN MULTI-PROCESSOR CLUSTERS OF  
MULTI-CLUSTER COMPUTER SYSTEMS

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Confirmation No.: 4184

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I hereby certify that this correspondence is being transmitted electronically through EFS-WEB to the  
Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450 on **November 26, 2007**

Signed: \_\_\_\_\_/Mary Deauclaire/  
Mary Deauclaire

**SEPARATE LETTER TO THE OFFICIAL DRAFTSMAN  
REQUESTING ENTERING OF FORMAL DRAWINGS  
(MPEP 608.02(p))**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Due to informalities in the original drawings, enclosed are replacement (formal) drawings, Fig 10A and Fig 12B, for the above-identified patent application. If the Draftsman has any question concerning these drawings, he or she is respectfully requested to contact the undersigned.

Respectfully submitted,  
BEYER WEAVER LLP

/Roger S. Sampson/

Roger S. Sampson  
Registration No. 44,314

P.O. Box 70250  
Oakland, CA 94612-0250  
(510) 663-1100